

# PolarHT™ Power MOSFET

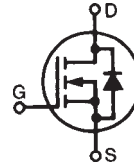
## IXTQ 200N06P

$$V_{DSS} = 60 \text{ V}$$

$$I_{D25} = 200 \text{ A}$$

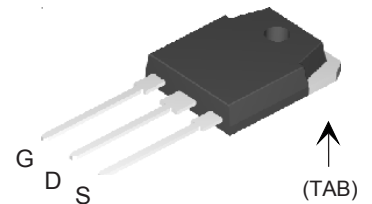
$$R_{DS(on)} \leq 6.0 \text{ m}\Omega$$

N-Channel Enhancement Mode  
Avalanche Rated



Symbol	Test Conditions	Maximum Ratings	
$V_{DSS}$	$T_J = 25^\circ\text{C to } 175^\circ\text{C}$	60	V
$V_{DGR}$	$T_J = 25^\circ\text{C to } 175^\circ\text{C}; R_{GS} = 1 \text{ M}\Omega$	60	V
$V_{GS}$	Transient	$\pm 30$	V
$V_{GSM}$	Continuous	$\pm 20$	V
$I_{D25}$	$T_C = 25^\circ\text{C}$	200	A
$I_{D(RMS)}$	External lead current limit	75	A
$I_{DM}$	$T_C = 25^\circ\text{C}$ , pulse width limited by $T_{JM}$	400	A
$I_{AR}$	$T_C = 25^\circ\text{C}$	60	A
$E_{AR}$	$T_C = 25^\circ\text{C}$	80	mJ
$E_{AS}$	$T_C = 25^\circ\text{C}$	4.0	J
$dv/dt$	$I_S \leq I_{DM}$ , $di/dt \leq 100 \text{ A}/\mu\text{s}$ , $V_{DD} \leq V_{DSS}$ , $T_J \leq 150^\circ\text{C}$ , $R_G = 4 \Omega$	10	V/ns
$P_D$	$T_C = 25^\circ\text{C}$	714	W
$T_J$		-55 ... +175	$^\circ\text{C}$
$T_{JM}$		175	$^\circ\text{C}$
$T_{stg}$		-55 ... +150	$^\circ\text{C}$
$T_L$	1.6 mm (0.062 in.) from case for 10 s	300	$^\circ\text{C}$
$T_{SOLD}$	Plastic body for 10 s	260	$^\circ\text{C}$
$M_d$	Mounting torque (TO-3P)	1.13/10	Nm/lb.in.
<b>Weight</b>	TO-3P	5.5	g

TO-3P (IXTQ)



G = Gate      D = Drain  
S = Source      TAB = Drain

### Features

- † International standard package
- † Unclamped Inductive Switching (UIS) rated
- † Low package inductance
- easy to drive and to protect

### Advantages

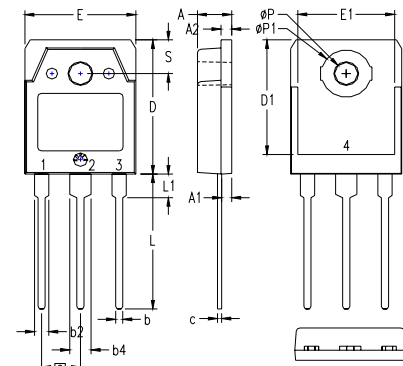
- † Easy to mount
- † Space savings
- † High power density

Symbol	Test Conditions ( $T_J = 25^\circ\text{C}$ , unless otherwise specified)	Characteristic Values		
		Min.	Typ.	Max.
$BV_{DSS}$	$V_{GS} = 0 \text{ V}$ , $I_D = 250 \mu\text{A}$	60		V
$V_{GS(th)}$	$V_{DS} = V_{GS}$ , $I_D = 250 \mu\text{A}$	2.5		5.0 V
$I_{GSS}$	$V_{GS} = \pm 20 \text{ V}_{DC}$ , $V_{DS} = 0$			$\pm 100 \text{ nA}$
$I_{DSS}$	$V_{DS} = V_{DSS}$ $V_{GS} = 0 \text{ V}$ $T_J = 150^\circ\text{C}$			25 $\mu\text{A}$ 250 $\mu\text{A}$
$R_{DS(on)}$	$V_{GS} = 10 \text{ V}$ , $I_D = 60 \text{ A}$ $V_{GS} = 15 \text{ V}$ , $I_D = 400 \text{ A}$ Pulse test, $t \leq 300 \mu\text{s}$ , duty cycle $d \leq 2 \%$		5.0	6.0 $\text{m}\Omega$ $\text{m}\Omega$

Symbol	Test Conditions	Characteristic Values ( $T_J = 25^\circ\text{C}$ , unless otherwise specified)		
		Min.	Typ.	Max.
$g_{fs}$	$V_{DS} = 10\text{ V}$ ; $I_D = 60\text{ A}$ , pulse test	45	65	S
$C_{iss}$ $C_{oss}$ $C_{rss}$	$V_{GS} = 0\text{ V}$ , $V_{DS} = 25\text{ V}$ , $f = 1\text{ MHz}$		5400	pF
			3550	pF
			1360	pF
$t_{d(on)}$ $t_r$ $t_{d(off)}$ $t_f$	$V_{GS} = 10\text{ V}$ , $V_{DS} = 0.5 V_{DSS}$ , $I_D = 60\text{ A}$ $R_G = 3.3\ \Omega$ (External)		35	ns
			60	ns
			90	ns
			40	ns
$Q_{g(on)}$ $Q_{gs}$ $Q_{gd}$	$V_{GS} = 10\text{ V}$ , $V_{DS} = 0.5 V_{DSS}$ , $I_D = 0.5 I_{D25}$		200	nC
			37	nC
			110	nC
$R_{thJC}$ $R_{thCS}$			0.21	$^\circ\text{C/W}$

Symbol	Test Conditions	Characteristic Values ( $T_J = 25^\circ\text{C}$ , unless otherwise specified)		
		Min.	Typ.	Max.
$I_S$	$V_{GS} = 0\text{ V}$			200 A
$I_{SM}$	Repetitive			400 A
$V_{SD}$	$I_F = I_S$ , $V_{GS} = 0\text{ V}$ , Pulse test, $t \leq 300\ \mu\text{s}$ , duty cycle $d \leq 2\%$			1.5 V
$t_{rr}$ $Q_{RM}$	$I_F = 25\text{ A}$ , $-di/dt = 100\text{ A}/\mu\text{s}$ $V_R = 30\text{ V}$ , $V_{GS} = 0\text{ V}$		90	ns
			1.0	$\mu\text{C}$

### TO-3P (IXTQ) Outline



- 1 - GATE
- 2 - DRAIN (COLLECTOR)
- 3 - SOURCE (EMITTER)
- 4 - DRAIN (COLLECTOR)

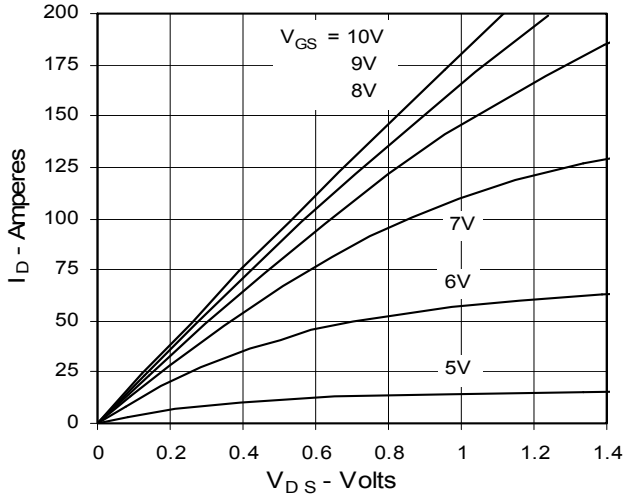
SYM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.185	.193	4.70	4.90
A1	.051	.059	1.30	1.50
A2	.057	.065	1.45	1.65
b	.035	.045	0.90	1.15
b2	.075	.087	1.90	2.20
b4	.114	.126	2.90	3.20
c	.022	.031	0.55	0.80
D	.780	.791	19.80	20.10
D1	.665	.677	16.90	17.20
E	.610	.622	15.50	15.80
E1	.531	.539	13.50	13.70
e	.215 BSC		5.45 BSC	
L	.779	.795	19.80	20.20
L1	.134	.142	3.40	3.60
phi P1	.126	.134	3.20	3.40
S	.193	.201	4.90	5.10

All metal area are tin plated.

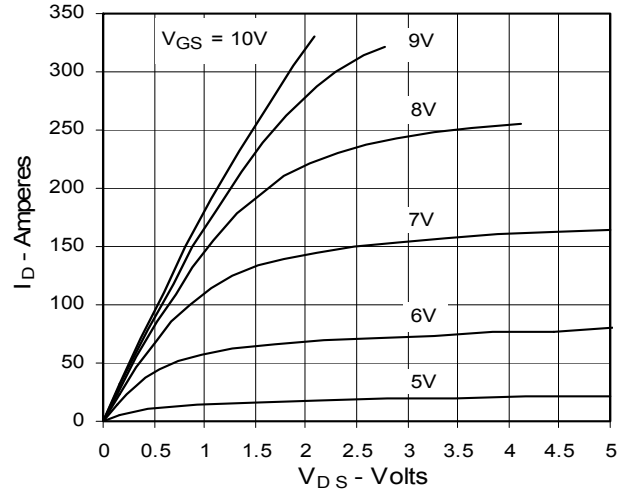
IXYS reserves the right to change limits, test conditions, and dimensions.

IXYS MOSFETs and IGBTs are covered by	4,835,592	4,931,844	5,049,961	5,237,481	6,162,665	6,404,065 B1	6,683,344	6,727,585
one or more of the following U.S. patents:	4,850,072	5,017,508	5,063,307	5,381,025	6,259,123 B1	6,534,343	6,710,405 B2	6,759,692
	4,881,106	5,034,796	5,187,117	5,486,715	6,306,728 B1	6,583,505	6,710,463	6,771,478 B2

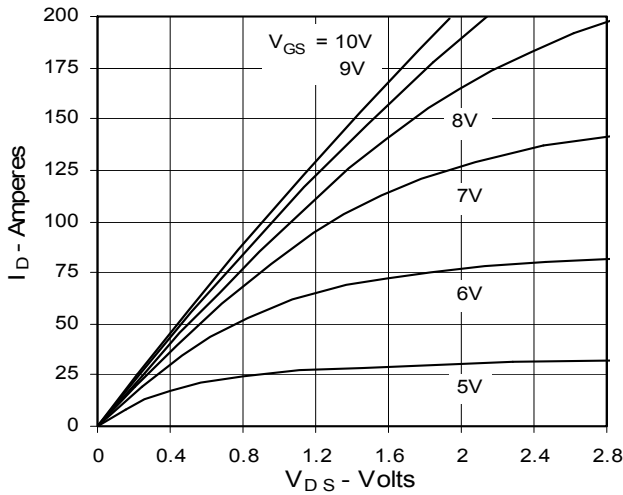
**Fig. 1. Output Characteristics @ 25°C**



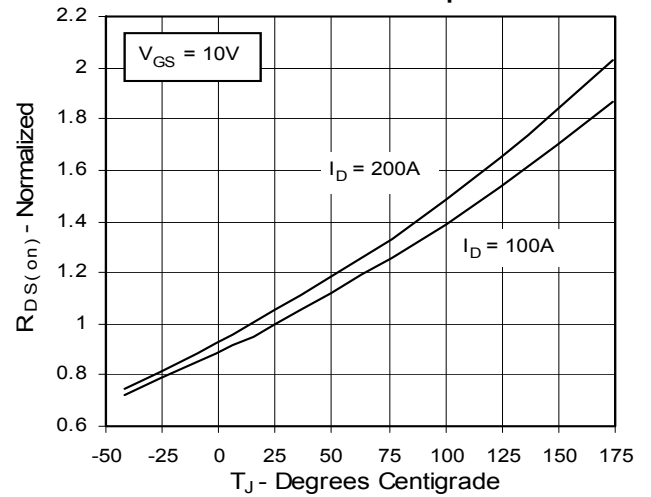
**Fig. 2. Extended Output Characteristics @ 25°C**



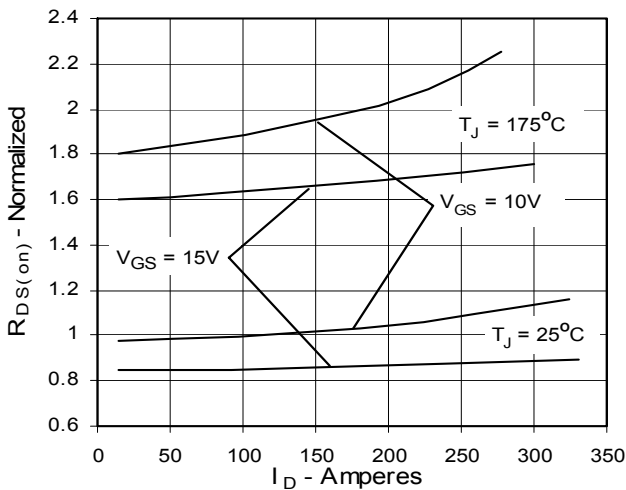
**Fig. 3. Output Characteristics @ 150°C**



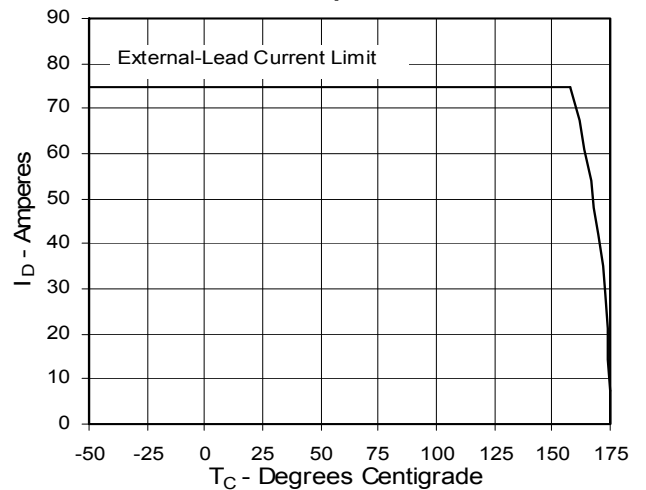
**Fig. 4.  $R_{DS(on)}$  Normalized to 0.5  $I_{D25}$  Value vs. Junction Temperature**



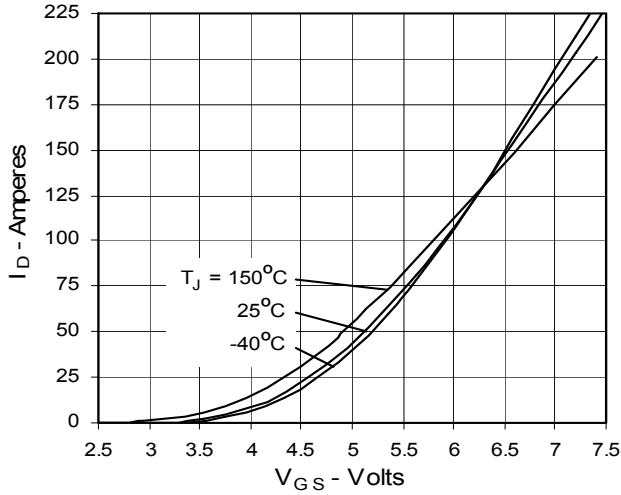
**Fig. 5.  $R_{DS(on)}$  Normalized to 0.5  $I_{D25}$  Value vs. Drain Current**



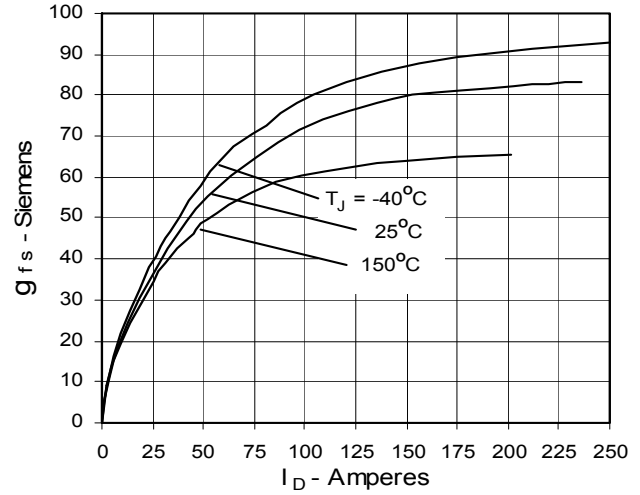
**Fig. 6. Drain Current vs. Case Temperature**



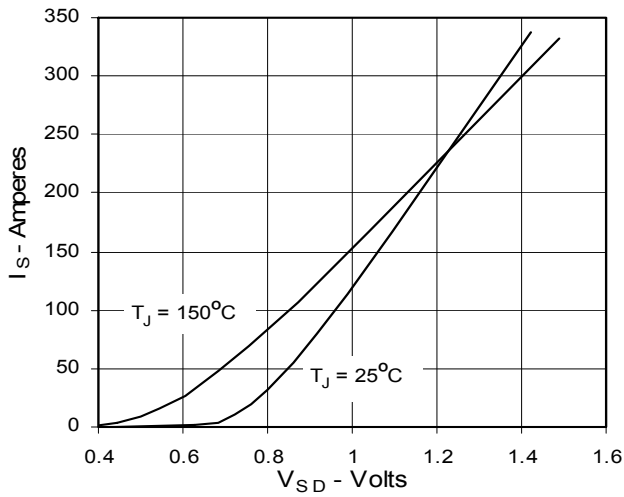
**Fig. 7. Input Admittance**



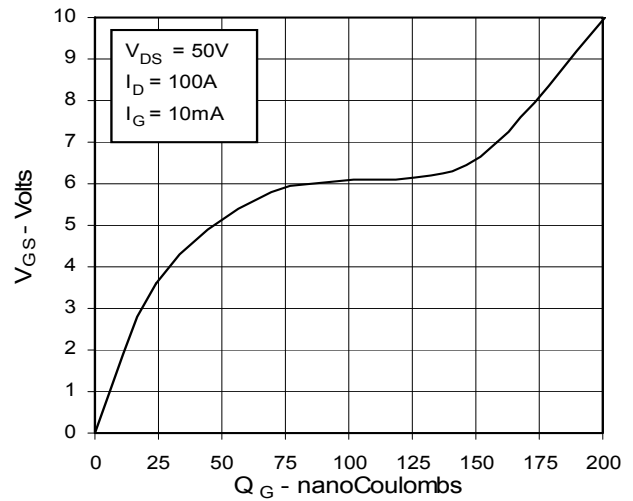
**Fig. 8. Transconductance**



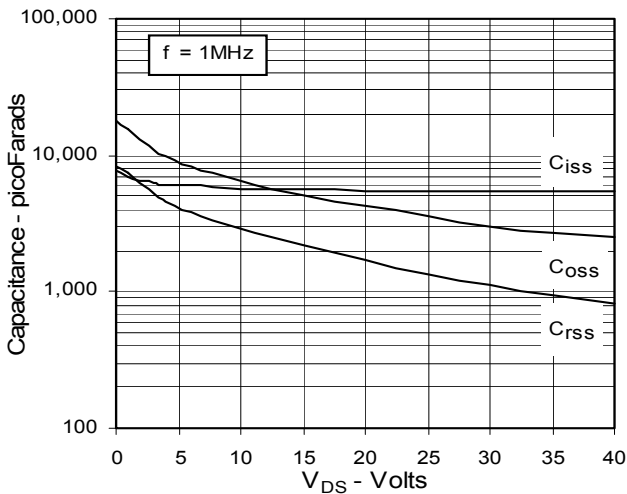
**Fig. 9. Source Current vs. Source-To-Drain Voltage**



**Fig. 10. Gate Charge**



**Fig. 11. Capacitance**



**Fig. 12. Forward-Bias Safe Operating Area**

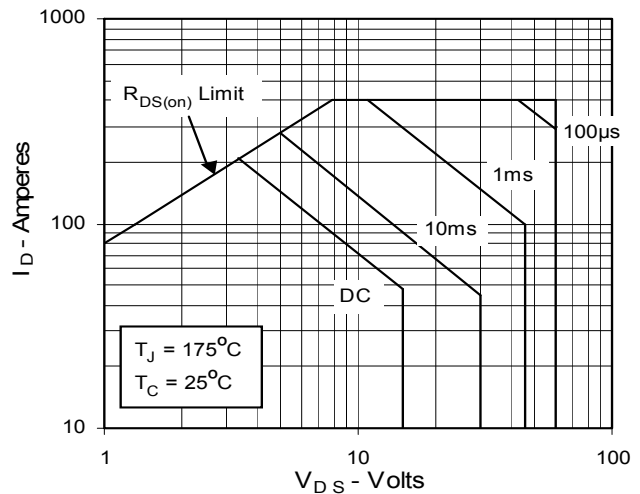


Fig. 13. Maximum Transient Thermal Resistance

